

CVD_RiscV Ver 1.13 (2022/12) Release Note

Division	Items	Contents
New	UI	About CVD menu show all supported license information
	UI	Support CodeViser board version 2.0
	UI	Can show higher address disassembly after virtual memory is on
	CMD	OPTION.MemAccess PB SB ABSTRACT
Fix	Firmware	Fixed can't show memory data on Fe310 board

CVD_RiscV Ver 1.12 (2022/03) Release Note

Division	Items	Contents
New	UI	About CVD menu show all supported license information
	UI	Support CodeViser board version 2.0
	UI	Support upper memory address data after mmu is on
Fix	Firmware	Fixed can't show memory data on Fe310 board

- CVD_RiscV Ver 1.11 (2021/10) Release Note

Division	Items	Contents
New	UI	Support SWD(Serial Wire Debugging) option
	UI	Added LowSpeed option for low system clock board
	CMD	Support Option.LOWSPEED ON/OFF command
Fix	Firmware	Fixed can't load saved window issue

- CVD_RiscV Ver 1.10 (2021/09) Release Note

Division	Items	Contents
New	Common	Support new core - RV32/RV64/N25/N45
	Common	Added 32bit Control & Status register read/write feature
	Common	Support dwarf5 format
	Common	General debugging features.
	Command	Added SETUP.DIS REGNAMES/ABINAMES